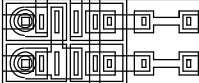
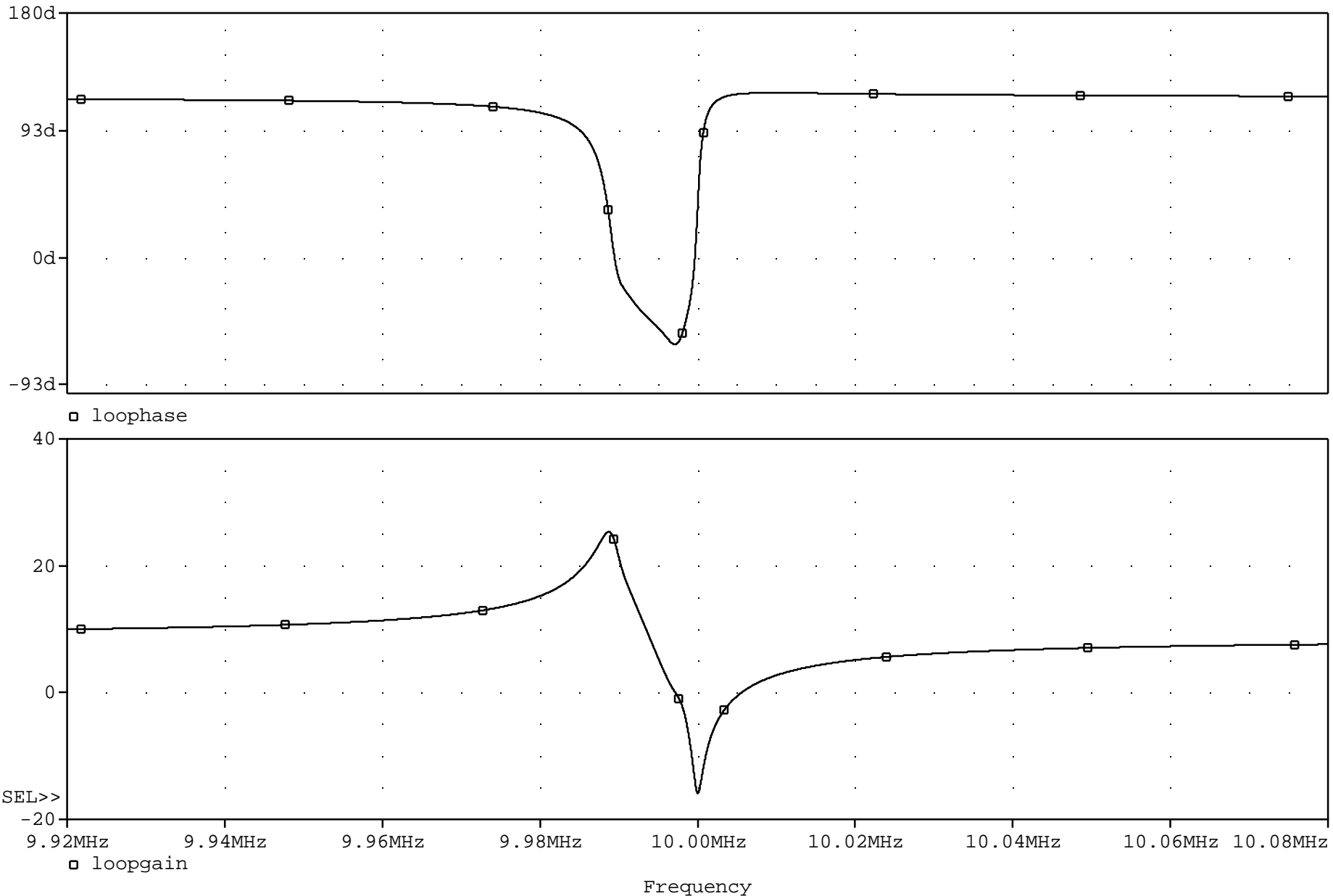


PARAMETERS:
 Shrink 0.8
 VCC 3.3

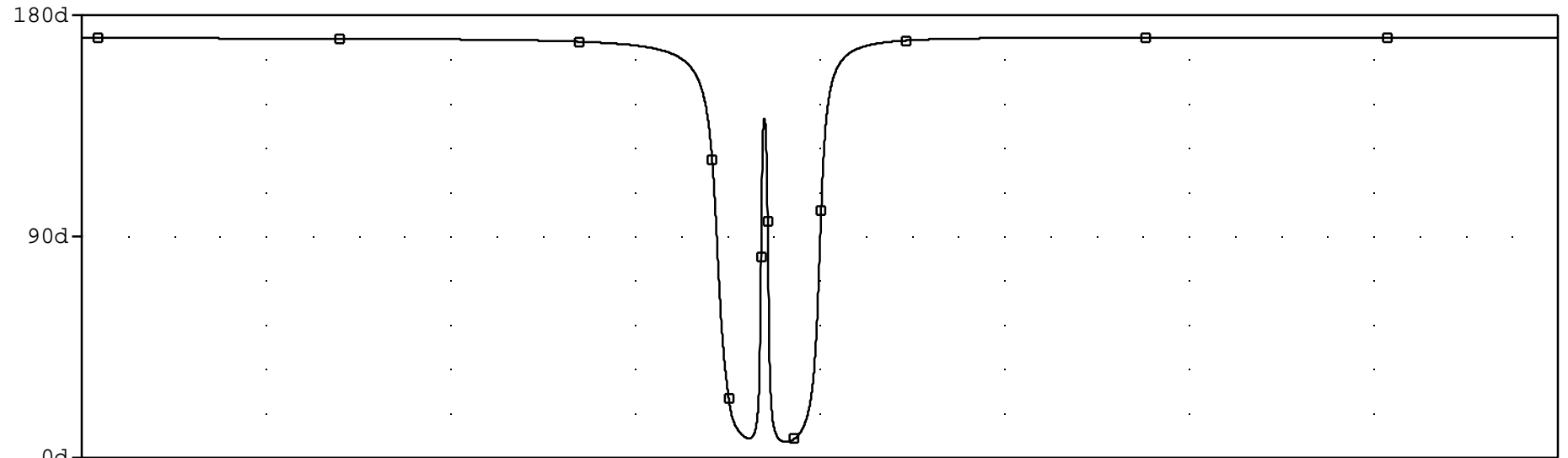
LIBRARY: C:\PSpice\DeviceLib\ON_Charter_0p6_5V\typi.inc
 LIBRARY: C:\PSpice\DeviceLib\ON_Charter_0p6_5V\UDR-Subcircuits.lib

		ANALOG INNOVATIONS, Inc. 824 E. CATHEDRAL ROCK DRIVE PHOENIX, AZ 85048-6300 (480) 460-2350 FAX: (480) 460-2142	
Title: Test Unbuffered Inverter			
Size A	FileName: ...TestUnBufInvXtalOsc.sch		REV A
February 8, 2004, 12:53 PM		Sheet 1 of 1	

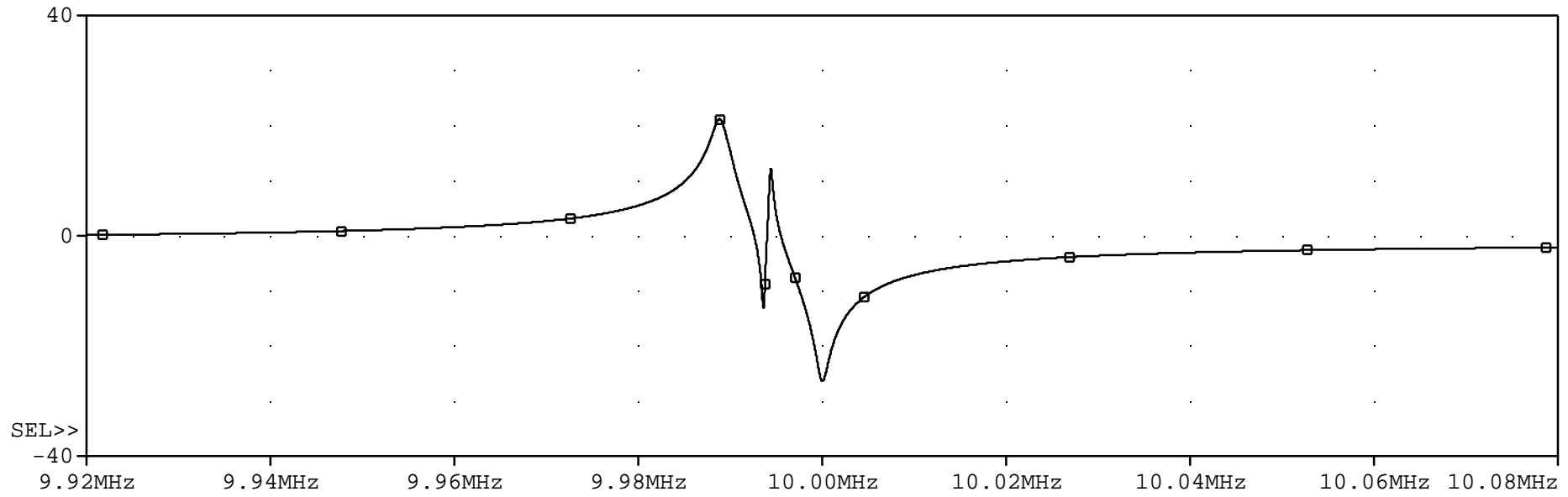
(A) TestUnBufInvXtalOsc... Loop Analysis with R Drive



(A) TestUnBufInvXtalOsc... Loop Analysis with C Drive -3pF



□ loophase

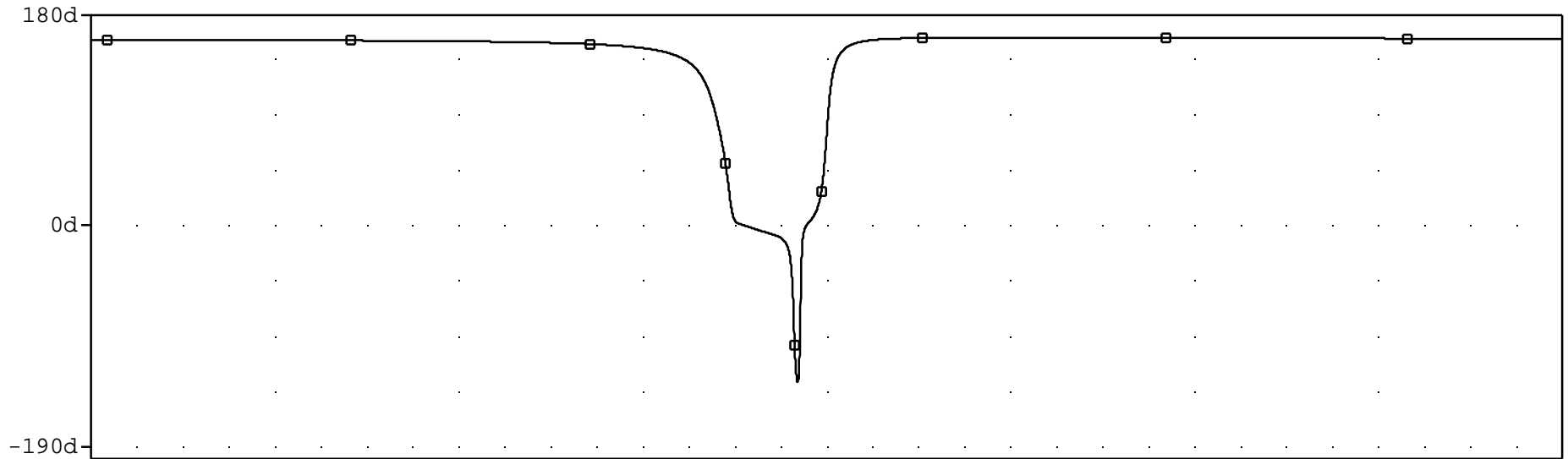


SEL>>

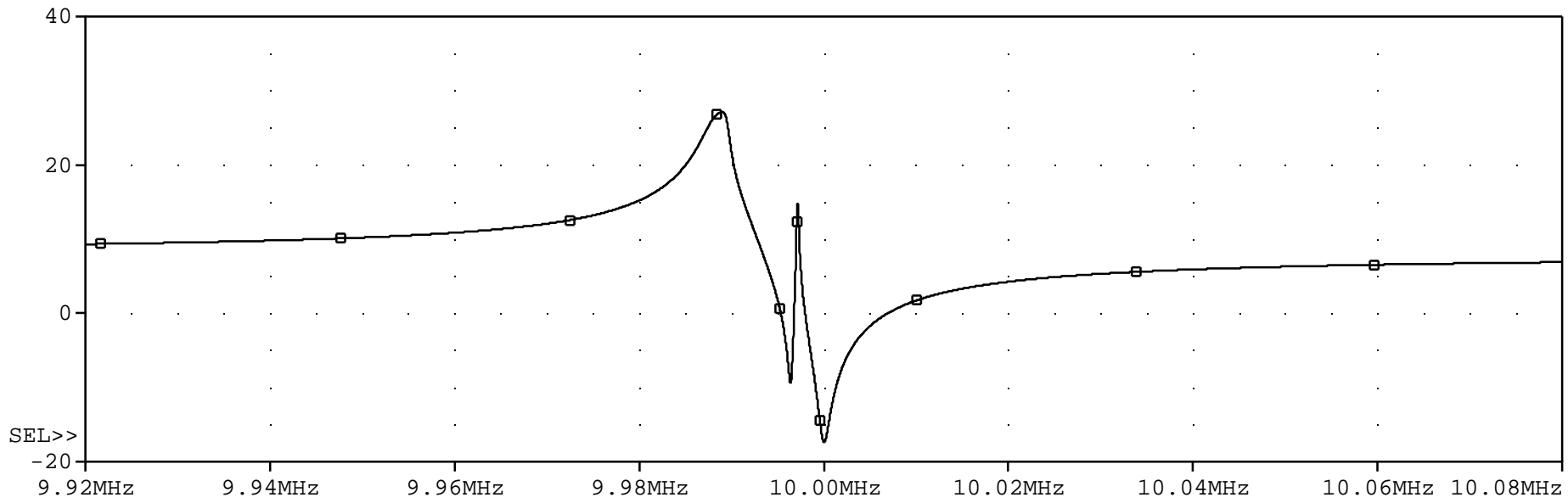
□ loopgain

Frequency

(A) TestUnBufInvXtalOsc... Loop Analysis with C Drive -15pF



□ loopphase

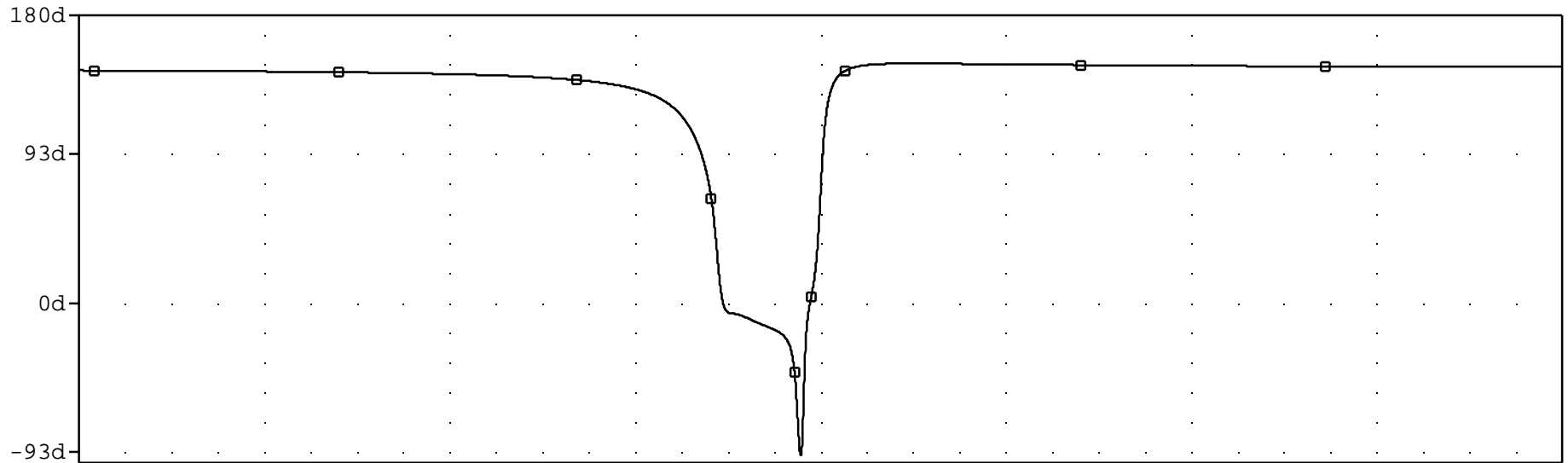


SEL>>

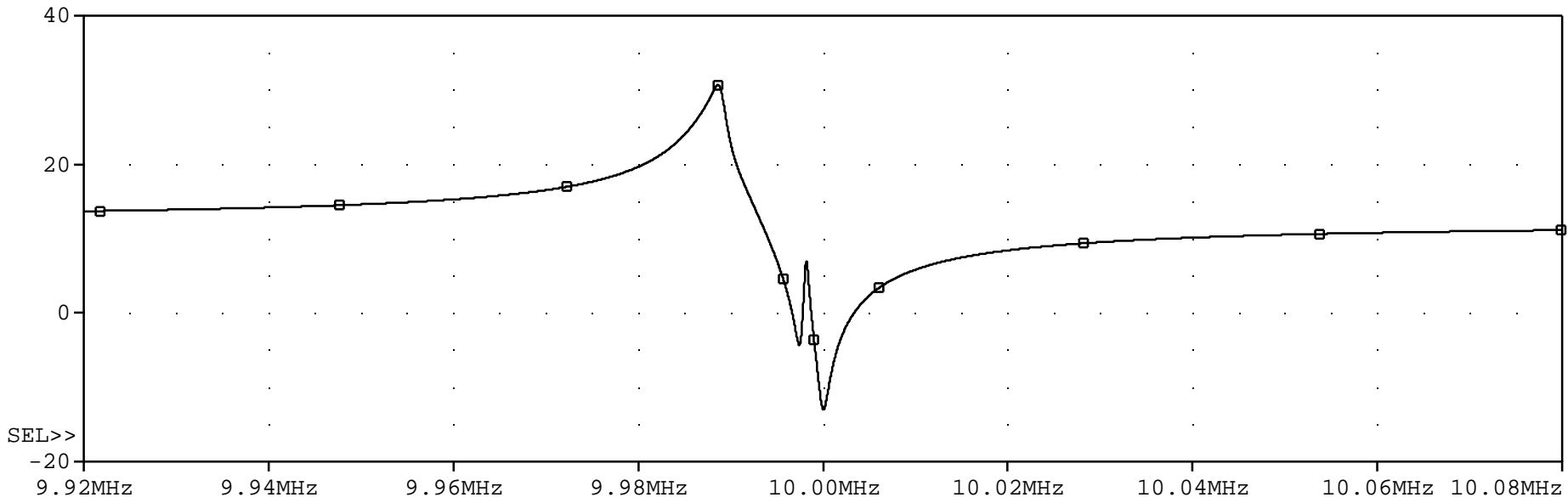
□ loopgain

Frequency

(A) TestUnBufInvXtalOsc... Loop Analysis with C Drive -100pF



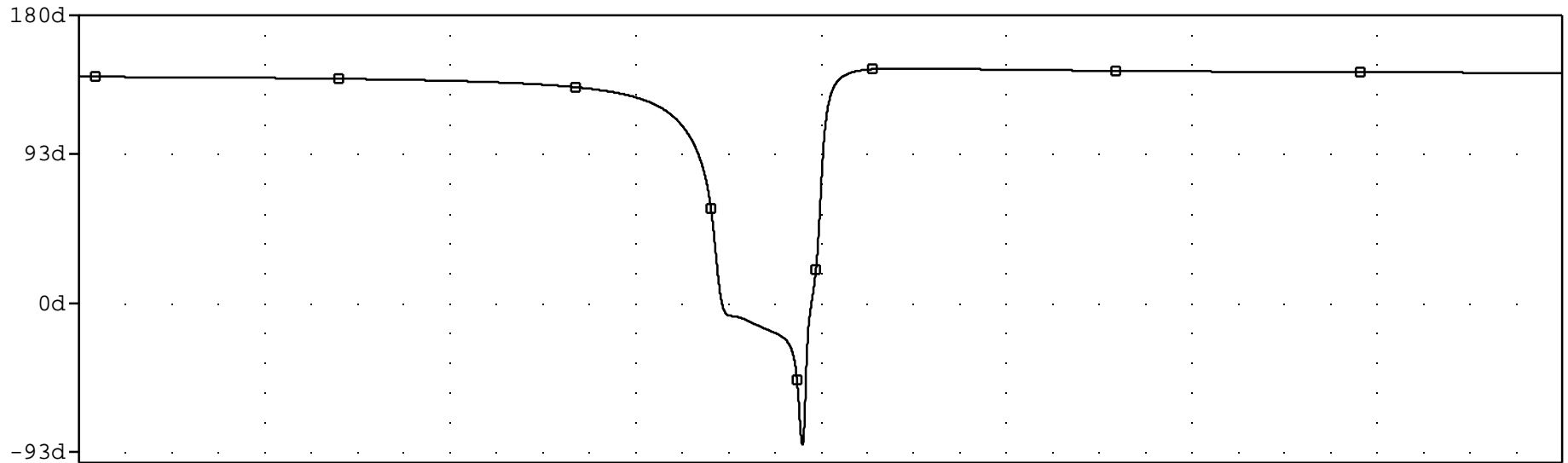
□ loopphase



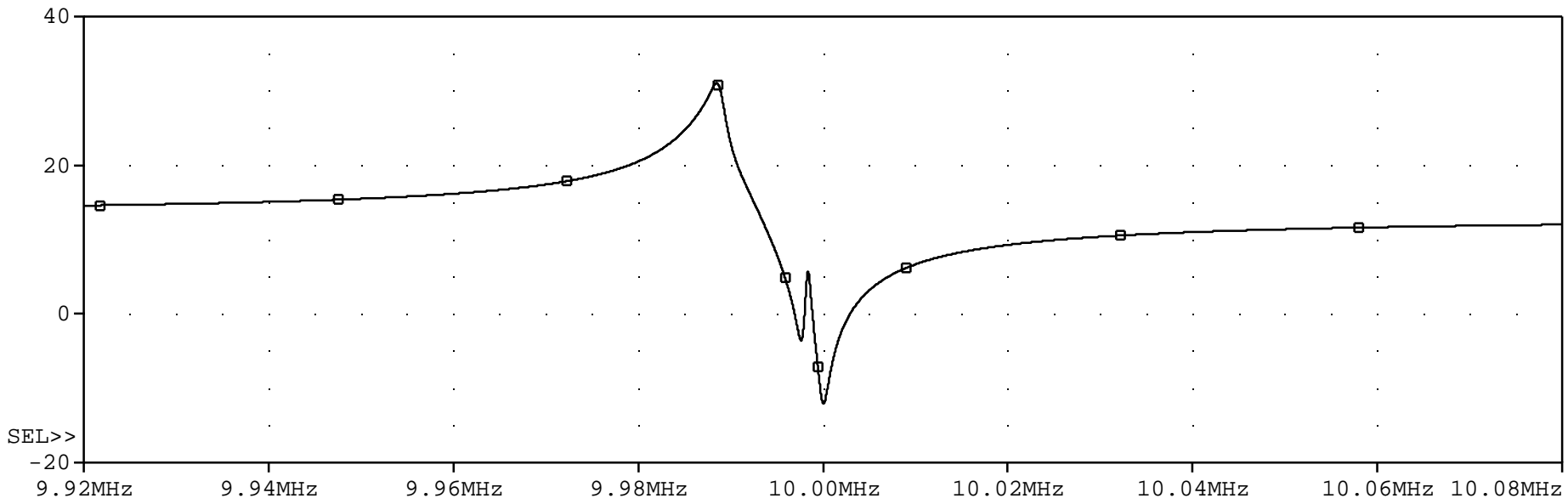
□ loopgain

Frequency

(A) TestUnBufInvXtalOsc... Loop Analysis with C Drive -lnF



□ loopphase



□ loopgain

Frequency